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Substitute for form 1449B/PTO				Complete if Known		
				Application Number	10/707,474	
INFORMATION DISCLOSURE			CLOSURE	Filing Date	12/17/2003	
STATEMENT BY APPLICANT				First Named Inventor	Ching-Hsiang Hsu	
JIAII			LIVAIII	Group Art Unit		
(use as many sheets as necessary)				Examiner Name		
Sheet	1	of	1	Attomey Docket Number	EMEP0056USA	

Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	ŀ			
7P	1	C.CH. HSU, et al.; A High Speed, Low Power P-Channel Flash EEPROM Using Silicon Rich Oxide as Tunneling Dielectric; Extended Abstracts of the 1992 International Conference on Solid State Device and Materials, Tsukuba, 1992, pp. 140-142				
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Examiner Signature	Trong Phan	Date Considered	6/13/04	

^{*}EXAMINER: Initial If reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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